FET Modeling for Circuit Simulation

Dileep A. Divekar

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JR Anderson

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FET Modeling for Circuit Simulation Dileep A. Divekar,2012-12-06 Circuit simulation is widely used for the design of circuits both discrete and integrated Device modeling is an impor tant aspect of circuit simulation since it is the link between the physical device and the sim ulate d device Curren tly available circuit simulation programs provide a variety of built in models Many circuit designers use these built in models whereas some incorporate new models in the circuit sim ulation programs Understanding device modeling with particular emphasis on circuit simulation will be helpful in utilizing the built in models more efficiently as well as in implementing new models SPICE is used as a vehicle since it is the most widely used circuit sim ulation program How ever some issues are addressed which are not directly appli cable to SPICE but are applicable to circuit simulation in general These discussions are useful for modifying SPICE and for understanding other simulation programs The gen eric version 2G 6 is used as a reference for SPICE although numerous different versions exist with different modifications This book describes field effect transistor models commonly used in a variety of circuit sim ulation pro grams Understanding of the basic device physics and some familiarity with device modeling is assumed Derivation of the model equations is not included SPICE is a circuit sim ulation program available from EECS Industrial Support Office 461 Cory Hall University of Cali fornia Berkeley CA 94720 Acknowledgements I wish to express my gratitude to Valid Logic Systems Inc **Fet Modeling for Circuit Simulation** Dileep A Divekar,1988-03-31 *Introduction to Device Modeling and Circuit Simulation* Tor A. Fjeldly,Trond Ytterdal,Michael S. Shur,1998 This book is a useful reference for practicing electrical engineers as well as a textbook for a junior senior or graduate level course in electrical engineering The authors combine two subjects device modeling and circuit simulation by providing a large number of well prepared examples of circuit simulations immediately following the description of many device models **Mosfet Modeling For Circuit Analysis And Design** Carlos Galup-montoro,Marcio Cherem Schneider,2007-02-27 This is the first book dedicated to the next generation of MOSFET models Addressed to circuit designers with an in depth treatment that appeals to device specialists the book presents a fresh view of compact modeling having completely abandoned the regional modeling approach Both an overview of the basic physics theory required to build compact MOSFET models and a unified treatment of inversion charge and surface potential models are provided The needs of digital analog and RF designers as regards the availability of simple equations for circuit designs are taken into account Compact expressions for hand analysis or for automatic synthesis valid in all operating regions are presented throughout the book All the main expressions for computer simulation used in the new generation compact models are derived Since designers in advanced technologies are increasingly concerned with fluctuations the modeling of fluctuations is strongly emphasized A unified approach for both space matching and time noise fluctuations is introduced *MOSFET Models for VLSI Circuit Simulation* Narain D. Arora,2012-12-06 Metal Oxide Semiconductor MOS transistors are the basic building block ofMOS integrated circuits I C

Very Large Scale Integrated VLSI circuits using MOS technology have emerged as the dominant technology in the semiconductor industry Over the past decade the complexity of MOS IC s has increased at an astonishing rate This is realized mainly through the reduction of MOS transistor dimensions in addition to the improvements in processing Today VLSI circuits with over 3 million transistors on a chip with effective or electrical channel lengths of 0 5 microns are in volume production Designing such complex chips is virtually impossible without simulation tools which help to predict circuit behavior before actual circuits are fabricated However the utility of simulators as a tool for the design and analysis of circuits depends on the adequacy of the device models used in the simulator This problem is further aggravated by the technology trend towards smaller and smaller device dimensions which increases the complexity of the models There is extensive literature available on modeling these short channel devices However there is a lot of confusion too Often it is not clear what model to use and which model parameter values are important and how to determine them After working over 15 years in the field of semiconductor device modeling I have felt the need for a book which can fill the gap between the theory and the practice of MOS transistor modeling This book is an attempt in that direction **Mosfet Modeling For Vlsi Simulation: Theory And Practice** Narain Arora,2007-02-14 A reprint of the classic text this book popularized compact modeling of electronic and semiconductor devices and components for college and graduate school classrooms and manufacturing engineering over a decade ago The first comprehensive book on MOS transistor compact modeling it was the most cited among similar books in the area and remains the most frequently cited today The coverage is device physics based and continues to be relevant to the latest advances in MOS transistor modeling This is also the only book that discusses in detail how to measure device model parameters required for circuit simulations The book deals with the MOS Field Effect Transistor MOSFET models that are derived from basic semiconductor theory Various models are developed ranging from simple to more sophisticated models that take into account new physical effects observed in submicron transistors used in today s 1993 MOS VLSI technology The assumptions used to arrive at the models are emphasized so that the accuracy of the models in describing the device characteristics are clearly understood Due to the importance of designing reliable circuits device reliability models are also covered Understanding these models is essential when designing circuits for state of the art MOS ICs **MOSFET Modeling & BSIM3 User’s Guide** Yuhua Cheng,Chenming Hu,2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User s Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of

compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User s Guide not only addresses the device modeling issues but also provides a user s guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User s Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike **A GaAs FET Model for Circuit Simulation** Peter James George,1987 **BSIM4 and MOSFET Modeling for IC Simulation** Weidong Liu,Chenming Hu,2011 This book presents the art of advanced MOSFET modeling for integrated circuit simulation and design It provides the essential mathematical and physical analyses of all the electrical mechanical and thermal effects in MOS transistors relevant to the operation of integrated circuits Particular emphasis is placed on how the BSIM model evolved into the first ever industry standard SPICE MOSFET model for circuit simulation and CMOS technology development The discussion covers the theory and methodology of how a MOSFET model or semiconductor device models in general can be implemented to be robust and efficient turning device physics theory into a production worthy SPICE simulation model Special attention is paid to MOSFET characterization and model parameter extraction methodologies making the book particularly useful for those interested or already engaged in work in the areas of semiconductor devices compact modeling for SPICE simulation and integrated circuit design **High Frequency MOSFET Modeling for Circuit Simulation** Suet Fong Tin,1998

Silicon And Beyond: Advanced Device Models And Circuit Simulators Tor A Fjeldly,Michael S Shur,2000-04-20 The steady downscaling of device feature size combined with a rapid increase in circuit complexity as well as the introduction of new device concepts based on non silicon material systems poses great challenges for device and circuit designers One of the major tasks is the development of new and improved device models needed for accurate device and circuit design Another task is the development of new circuit simulation tools to handle very large and complex circuits This book addresses both these issues with up to date reviews written by leading experts in the field The first three chapters of the book discuss advanced device models both for existing technologies and for new emerging technologies Among the topics covered are models for MOSFETs thin film transitors TFTs and compound semiconductor devices including GaAs HEMTs and HFETs heterodimensional devices quantum tunneling devices as well as wide bandgap devices Chapters 4 and 5 discuss advanced circuit simulators that hold promise for handling circuits of much higher complexity than what is possible for typical state of

the art circuit simulators today **Modeling and Characterization of RF and Microwave Power FETs** Peter Aaen,Jaime A. Plá,John Wood,2007-06-25 This book is a comprehensive exposition of FET modeling and is a must have resource for seasoned professionals and new graduates in the RF and microwave power amplifier design and modeling community In it you will find descriptions of characterization and measurement techniques analysis methods and the simulator implementation model verification and validation procedures that are needed to produce a transistor model that can be used with confidence by the circuit designer Written by semiconductor industry professionals with many years device modeling experience in LDMOS and III V technologies this was the first book to address the modeling requirements specific to high power RF transistors A technology independent approach is described addressing thermal effects scaling issues nonlinear modeling and in package matching networks These are illustrated using the current market leading high power RF technology LDMOS as well as with III V power devices Physics And Modeling Of Mosfets, The: Surface-potential Model Hisim Tatsuya Ezaki,Hans Jurgen Mattausch,Mitiko Miura-mattausch,2008-06-03 This volume provides a timely description of the latest compact MOS transistor models for circuit simulation The first generation BSIM3 and BSIM4 models that have dominated circuit simulation in the last decade are no longer capable of characterizing all the important features of modern sub 100nm MOS transistors This book discusses the second generation MOS transistor models that are now in urgent demand and being brought into the initial phase of manufacturing applications It considers how the models are to include the complete drift diffusion theory using the surface potential variable in the MOS transistor channel in order to give one characterization equation **Carbon Nanotube Electronics** Ali Javey,Jing Kong,2009-04-21 This book provides a complete overview of the field of carbon nanotube electronics It covers materials and physical properties synthesis and fabrication processes devices and circuits modeling and finally novel applications of nanotube based electronics The book introduces fundamental device physics and circuit concepts of 1 D electronics At the same time it provides specific examples of the state of the art nanotube devices **MOSFET Modeling with SPICE** Daniel Foty,1997 This book will help CMOS circuit designers make the best possible use of SPICE models and will prepare them for new models that may soon be introduced Introduces SPICE modeling and its use in CMOS circuit design Presents the formalism of model building and the semiconductor physics of MOS structures Covers each important SPICE model showing how to choose the appropriate model Discusses the popular HSPICE Level 28 as well as Levels 1 3 BSIM 1 3 and MOS Model 9 Presents techniques for accounting for systematic process variations Describes new model candidates including the Power Lane Model the PCIM Model and the EKV Model Includes extensive examples throughout Practicing engineers and scientists in the semiconductor industry engineering faculty and students **Cmos Rf Modeling, Characterization And Applications** M Jamal Deen,Tor A Fjeldly,2002-04-10 CMOS technology has now reached a state of evolution in terms of both frequency and noise where it is becoming a serious contender for radio frequency RF applications in the GHz range Cutoff frequencies of about 50 GHz have

been reported for 0 18 m CMOS technology and are expected to reach about 100 GHz when the feature size shrinks to 100 nm within a few years This translates into CMOS circuit operating frequencies well into the GHz range which covers the frequency range of many of today s popular wireless products such as cell phones GPS Global Positioning System and Bluetooth Of course the great interest in RF CMOS comes from the obvious advantages of CMOS technology in terms of production cost high level integration and the ability to combine digital analog and RF circuits on the same chip This book discusses many of the challenges facing the CMOS RF circuit designer in terms of device modeling and characterization which are crucial issues in circuit simulation and design *FinFET Modeling for IC Simulation and Design* Yogesh Singh Chauhan,Darsen Lu,Sriramkumar Vanugopalan,Sourabh Khandelwal,Juan Pablo Duarte,Navid Payvadosi,Ali Niknejad,Chenming Hu,2015-03-17 This book is the first to explain FinFET modeling for IC simulation and the industry standard BSIM CMG describing the rush in demand for advancing the technology from planar to 3D architecture as now enabled by the approved industry standard The book gives a strong foundation on the physics and operation of FinFET details aspects of the BSIM CMG model such as surface potential charge and current calculations and includes a dedicated chapter on parameter extraction procedures providing a step by step approach for the efficient extraction of model parameters With this book you will learn Why you should use FinFET The physics and operation of FinFET Details of the FinFET standard model BSIM CMG Parameter extraction in BSIM CMG FinFET circuit design and simulation Authored by the lead inventor and developer of FinFET and developers of the BSIM CM standard model providing an experts insight into the specifications of the standard The first book on the industry standard FinFET model BSIM CMG **The Physics and Modeling of Mosfets** Mitiko Miura-Mattausch,2008 This volume provides a timely description of the latest compact MOS transistor models for circuit simulation The first generation BSIM3 and BSIM4 models that have dominated circuit simulation in the last decade are no longer capable of characterizing all the important features of modern sub 100nm MOS transistors This book discusses the second generation MOS transistor models that are now in urgent demand and being brought into the initial phase of manufacturing applications It considers how the models are to include the complete drift diffusion theory using the surface potential variable in the MOS transistor channel in order to give one characterization equation

Transistor Level Modeling for Analog/RF IC Design Wladyslaw Grabinski,Bart Nauwelaers,Dominique Schreurs,2006-07-01 Among many great inventions made in the 20th century electronic circuits which later evolved into integrated circuits are probably the biggest when considering their contribution to human society Entering the 21st century the importance of integrated circuits has increased even more In fact without the help of integrated circuits recent high technology society with the internet cellular phone car navigation digital camera and robot would never have been realized Nowadays integrated circuits are indispensable for almost every activity of our society One of the critical issues for the fabrication of integrated circuits has been the precise design of the high speed or high frequency operation of circuits with

huge number of components It is quite natural to predict the circuit operation by computer calculation and there have been three waves for this at 15 year intervals The rst wave came at the beginning of the 1970s when LSIs Large Scale Integrated circuits with more than 1000 components had just been int duced into the market A mainframe computer was used for the simulation and each semiconductor company used its own proprietary simulators and device models However the capability of the computer and accuracy of the model were far from satisfactory and there are many cases of the necessity of circuit re design after evaluation of the rst chip The second wave hit us in the middle of 1980s when the EWS Engine ing Work Station was introduced for use by designers *Unified Field Effect Transistor Modeling and Characterization for Computer-aided Design* Byung-Jong Moon,1991

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