

João M. P. Cardoso · Michael Hübner  
*Editors*

# Reconfigurable Computing

From FPGAs to Hardware/  
Software Codesign



Springer

# Reconfigurable Computing From Fpgas To Hardwaresoftware Codesign

**Scott Hauck, André DeHon**



## **Reconfigurable Computing From Fpgas To Hardwaresoftware Codesign:**

**Reconfigurable Computing** Joao Cardoso, Michael Hübner, 2011-08-17 As the complexity of modern embedded systems increases it becomes less practical to design monolithic processing platforms As a result reconfigurable computing is being adopted widely for more flexible design Reconfigurable Computers offer the spatial parallelism and fine grained customizability of application specific circuits with the postfabrication programmability of software To make the most of this unique combination of performance and flexibility designers need to be aware of both hardware and software issues FPGA users must think not only about the gates needed to perform a computation but also about the software flow that supports the design process The goal of this book is to help designers become comfortable with these issues and thus be able to exploit the vast opportunities possible with reconfigurable logic *Reconfigurable Computing* Scott Hauck, André DeHon, 2010-07-26 Reconfigurable Computing marks a revolutionary and hot topic that bridges the gap between the separate worlds of hardware and software design the key feature of reconfigurable computing is its groundbreaking ability to perform computations in hardware to increase performance while retaining the flexibility of a software solution Reconfigurable computers serve as affordable fast and accurate tools for developing designs ranging from single chip architectures to multi chip and embedded systems Scott Hauck and Andre DeHon have assembled a group of the key experts in the fields of both hardware and software computing to provide an introduction to the entire range of issues relating to reconfigurable computing FPGAs field programmable gate arrays act as the computing vehicles to implement this powerful technology Readers will be guided into adopting a completely new way of handling existing design concerns and be able to make use of the vast opportunities possible with reconfigurable logic in this rapidly evolving field Designed for both hardware and software programmers Views of reconfigurable programming beyond standard programming languages Broad set of case studies demonstrating how to use FPGAs in novel and efficient ways **Reconfigurable computing and hardware/software codesign** Toomas P. Plaks, Marco D. Santambrogio, Donatella Sciuto, 2008 Readings in Hardware/Software Co-Design Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2002 This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s **Hardware/Software Co-design for Heterogeneous Multi-core Platforms** Koen Bertels, 2012-02-02 HW SW Co Design for Heterogeneous Multi Core Platforms describes the results and outcome of the FP6 project which focuses on the development of an integrated tool chain targeting a heterogeneous multi core platform comprising of a general purpose processor ARM or powerPC a DSP the diopsis and an FPGA The tool chain takes existing source code and proposes transformations and mappings such that legacy code can easily be ported to a modern multi core platform Downloadable software will be provided for simulation purposes Context-Aware Systems and Applications Phan Cong Vinh, Abdur Rakib, 2022-01-06 This book constitutes the refereed post conference proceedings of the International Conference on Context

Aware Systems and Applications held in October 2021 Due to COVID 19 pandemic the conference was held virtually The 25 revised full papers presented were carefully selected from 52 submissions The papers cover a wide spectrum of modern approaches and techniques for smart computing systems and their applications

**Field-Programmable Logic and Applications** Peter Y.K. Cheung, Georg A. Constantinides, 2003-08-27 This book constitutes the refereed proceedings of the 13th International Conference on Field Programmable Logic and Applications FPL 2003 held in Lisbon Portugal in September 2003 The 90 revised full papers and 56 revised poster papers presented were carefully reviewed and selected from 216 submissions The papers are organized in topical sections on technologies and trends communications applications high level design tools reconfigurable architecture cryptographic applications multi context FPGAs low power issues run time reconfiguration compilation tools asynchronous techniques bio related applications codesign reconfigurable fabrics image processing applications SAT techniques application specific architectures DSP applications dynamic reconfiguration SoC architectures emulation cache design arithmetic bio inspired design SoC design cellular applications fault analysis and network applications

**Field Programmable Gate Arrays (FPGAs) for Fast Board Development and Reconfigurable Computing** John Schewel, Society of Photo-optical Instrumentation Engineers, 1995

**Application Hardware-software Co-design for Reconfigurable Computing Systems**, 2008 Application hardware software co design for reconfigurable computing systems

**Field-programmable Logic and Applications**, 2002

**Configurable Computing**, 1998 This collection of papers presented at the IS T SPIE Electronic Imaging Symposium includes articles on a variety of relevant issues and topics

**Reconfigurable Technology**, 2000

**IEEE/ACM/IFIP International Conference on Hardware/Software Codesign & System Synthesis**, 2005

Fourth International Workshop on Hardware/Software Co-Design, Codes/CASHE '96 Donald E. Thomas, Rolf Ernst, 1996 Embedded architecture co synthesis and system integration B Lin S Vercauteren and H De Man A multi level transformation approach to HW SW codesign a case study T K Y Cheung G Hellestrand and P Kanthamanon Fully parallel hardware software codesign for multi dimensional DSP applications M Sheliga N L Passos and E H M Sha A co design methodology based on formal specification and high level estimation C Carreras and others Speed up estimation for HW SW systems W Hardt and W Rosenstiel A framework for interactive analysis of timing constraints in embedded systems R K Gupta The interplay of run time estimation and granularity in HW SW partitioning J Henkel and R Ernst Partitioning and exploration strategies in the TOSCA co design flow A Balboni W Fornaciari and D Sciuto Process partitining for distributed embedded systems J Hou and W Wolf Two level partitioning of image processing algorithms for the parallel map oriented machine R W Hartenstein J Becker and R Kress PACE a dynamic programming algorithm for hardware software partitioning P V Knudsen and J Madsen A model for the coanalysis of hardware and software architectures F Rose and others A case study in co design of communication controllers R Gerndt Formal verification of embedded systems based on CFSM networks F Balarin and others Towards a model for hardware and software functional

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