# Iterative Arrays of Logical Circuits

Frederick C. Hennie, III

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# Iterative Arrays Of Logical Circuits

Shasha Hu

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**ITERATIVE ARRAYS OF LOGICAL CIR- CIRCUITS** ,1961 Scientific and Technical Aerospace Reports ,1967 Lists citations with abstracts for aerospace related reports obtained from world wide sources and announces documents that have recently been entered into the NASA Scientific and Technical Information Database *Recent Developments in Switching Theory* Amar Mukhopadhyay,2013-10-22 Electrical Science Series Recent Developments in Switching Theory covers the progress in the study of the switching theory The book discusses the simplified proof of Post s theorem on completeness of logic primitives the role of feedback in combinational switching circuits and the systematic procedure for the design of Lupanov decoding networks The text also describes the classical results on counting theorems and their application to the classification of switching functions under different notions of equivalence including linear and affine equivalences The development of abstract harmonic analysis of combinational switching functions the theory of universal logic modules methods of their construction and upper bounds on the input terminals and cellular logic are also considered The book further tackles the systematic techniques for the realization of multi output logic function by means of multirail cellular cascades the programmable cellular logic and the logical design of programmable arrays Electrical engineers electronics engineers computer professionals and student taking related courses will find the book invaluable A Functional Description of the Edvac [an Automatically-sequence Serial Binary Electronic Digital Computer Moore School of Electrical Engineering,1949 *The Essence of Logic Circuits* Stephen H. Unger,1997 Today designing a state of the art circuit means knowing how to pack more and more logic on a chip Featuring an extensive introductory material this complete carefully organized guide brings you valuable information on designing modern logic circuits from gates switches and other basic elements to meet the rising demands on modern circuit technology THE ESSENCE OF LOGIC CIRCUITS allows computer scientists and students to start from scratch and gain a comprehensive understanding of most important topics in the field

**Fault Detection in Digital Circuits** Arthur D. Friedman,Premachandran R. Menon,1971 *Space Communications: Theory and Applications: Information processing and advanced techniques* ,1965 **NASA SP.** ,1965 **Space Communications- Theory and Applications. Volume 3- Information Processing and Advanced Techniques a Bibliography, 1958 - 1963** ,1965 *IEEE Conference Record of 1966 Seventh Annual Symposium on Switching and Automata Theory* ,1966 **I.E.E.E. Conference Record of ... Annual Symposium on Switching and Automata Theory**

,1966 *Automata Networks* Christian Choffrut,1988 Logic and Computer Design Fundamentals M. Morris Mano,Charles R. Kime,2008 Featuring a strong emphasis on the fundamentals underlying contemporary logic design using hardware description languages synthesis and verification this book focuses on the ever evolving applications of basic computer design concepts with strong connections to real world technology Treatment of logic design digital system design and computer design Ideal for self study by engineers and computer scientists **Control Abstracts** ,1966 **Testing of Digital Systems** N. K. Jha,S. Gupta,2003-05-08 Device testing represents the single largest manufacturing expense in the semiconductor industry costing over 40 billion a year The most comprehensive and wide ranging book of its kind Testing of Digital Systems covers everything you need to know about this vitally important subject Starting right from the basics the authors take the reader through automatic test pattern generation design for testability and built in self test of digital circuits before moving on to more advanced topics such as IDDQ testing functional testing delay fault testing memory testing and fault diagnosis The book includes detailed treatment of the latest techniques including test generation for various fault models discussion of testing techniques at different levels of integrated circuit hierarchy and a chapter on system on a chip test synthesis Written for students and engineers it is both an excellent senior graduate level textbook and a valuable reference

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